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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 09/704,563 11/03/2000 PAT 6,635,512 *Yes RR*

** FOREIGN APPLICATIONS *****

JAPAN 11-314086 11/04/1999
 JAPAN 11-314087 11/04/1999
 JAPAN 11-314088 11/04/1999

Yes RR

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 04/21/2006

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no				
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	<i>[Signature]</i> Examiner's Signature <i>[Initials]</i> Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 8	TOTAL CLAIMS 12	INDEPENDENT CLAIMS 1

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TITLE

Method of producing a semiconductor device by dividing a semiconductor wafer into separate pieces of semiconductor chips

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